



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Koichi TERASHIMA, et al.  
Title: SEMICONDUCTOR DEVICE AND  
MANUFACTURING PROCESS THEREFOR  
Appl. No.: 10/576,412  
Filing Date: 4/19/2006  
Examiner: Allan R. Wilson  
Art Unit: 2815  
Confirmation Number: 3430

**AMENDMENT AND REPLY UNDER 37 CFR 1.111**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This communication is responsive to the Non-Final Office Action dated April 14, 2009, concerning the above-referenced patent application.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 2 of this document.

**Remarks/Arguments** begin on page 8 of this document.

Please amend the application as follows: